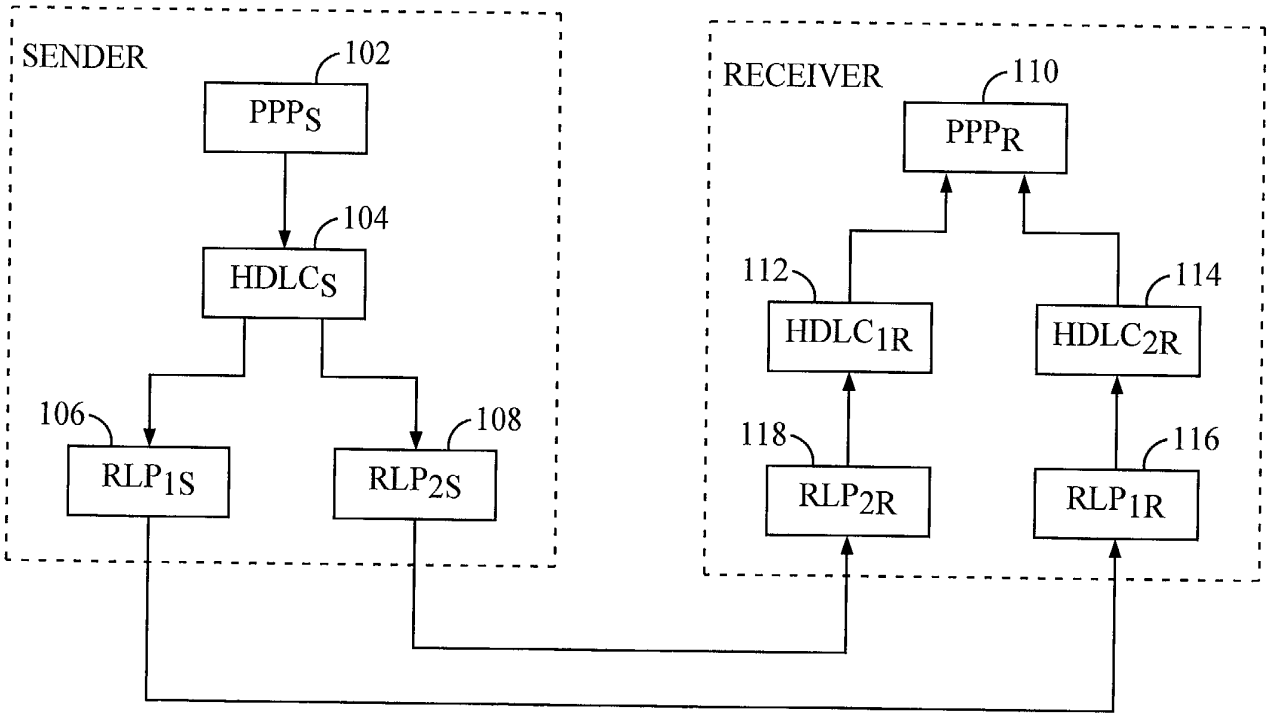
SENDER
102
PPPS
104
HDLCS
106
RLP1S
108
RLP2S
RECEIVER
110
PPPR
112
HDLC1R
114
HDLC2R
118
RLP2R
116
RLP1R

FIG. 1

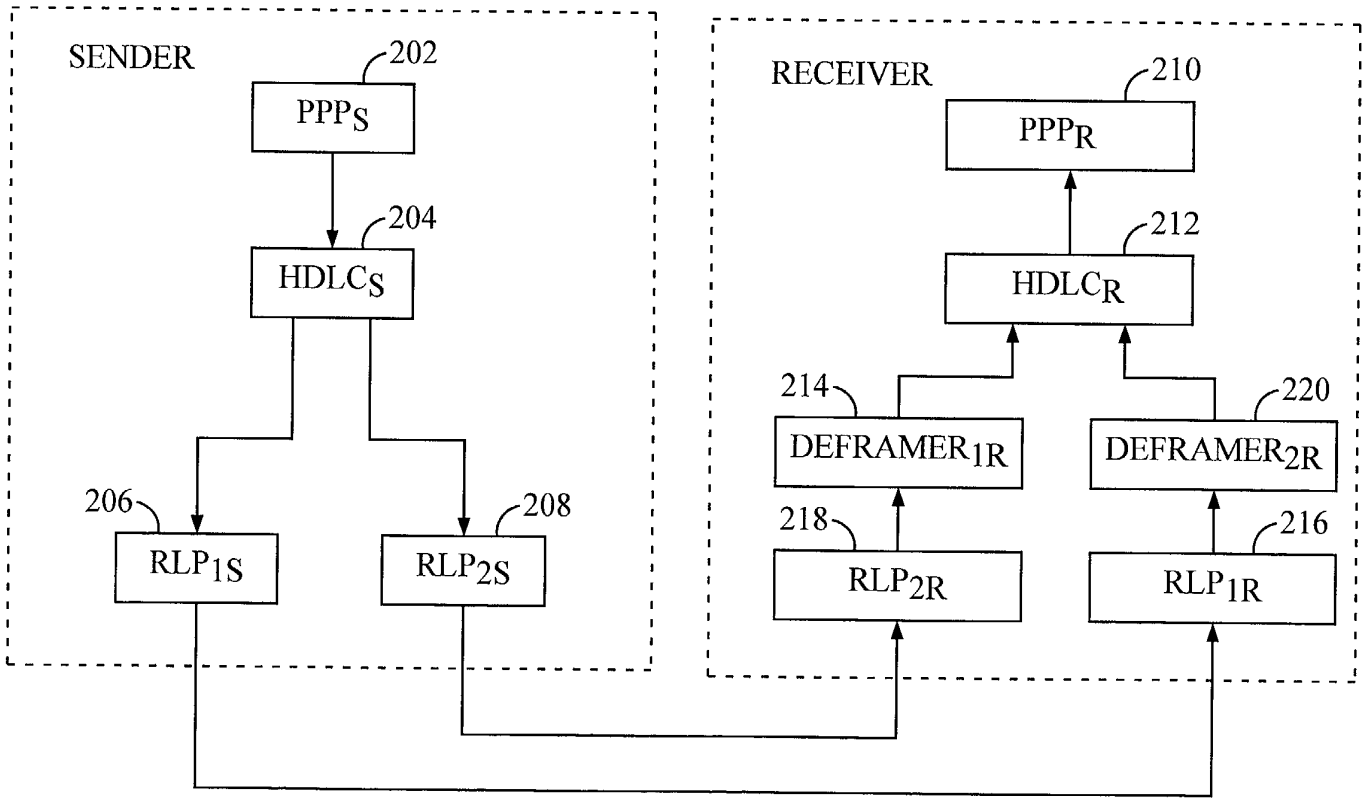

FIG. 2

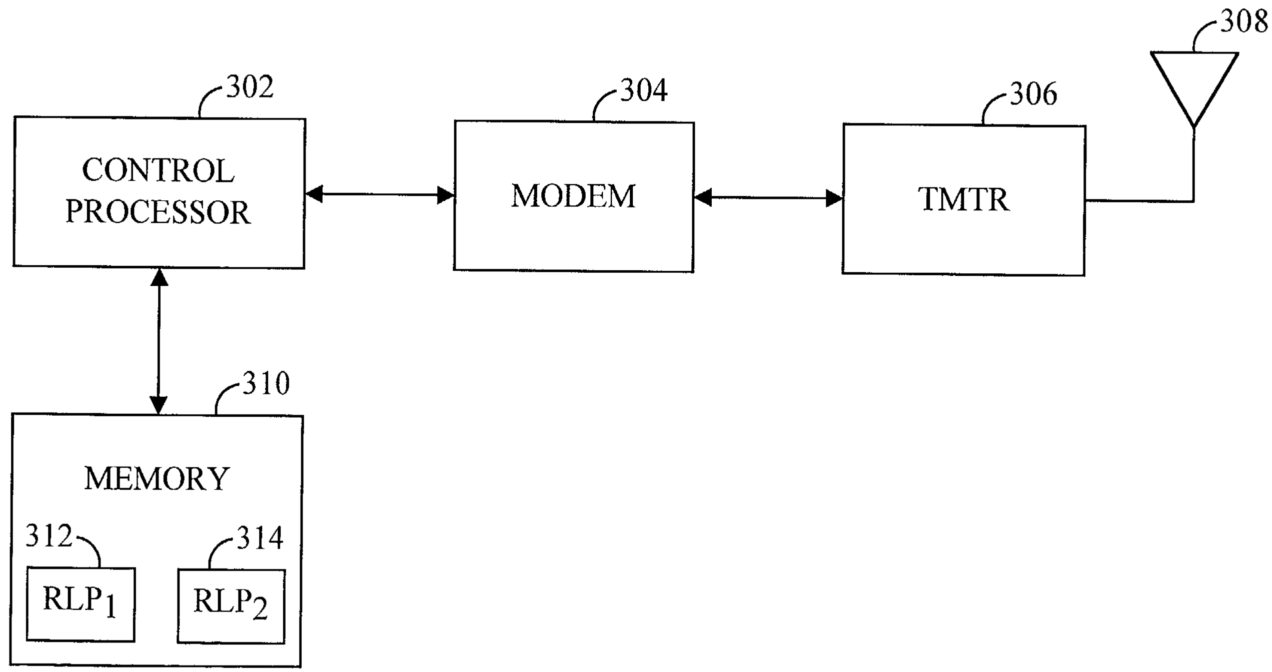
302
CONTROL PROCESSOR
304
MODEM
306
TMTR
308
310
MEMORY
312
RLP1
314
RLP2

FIG. 3

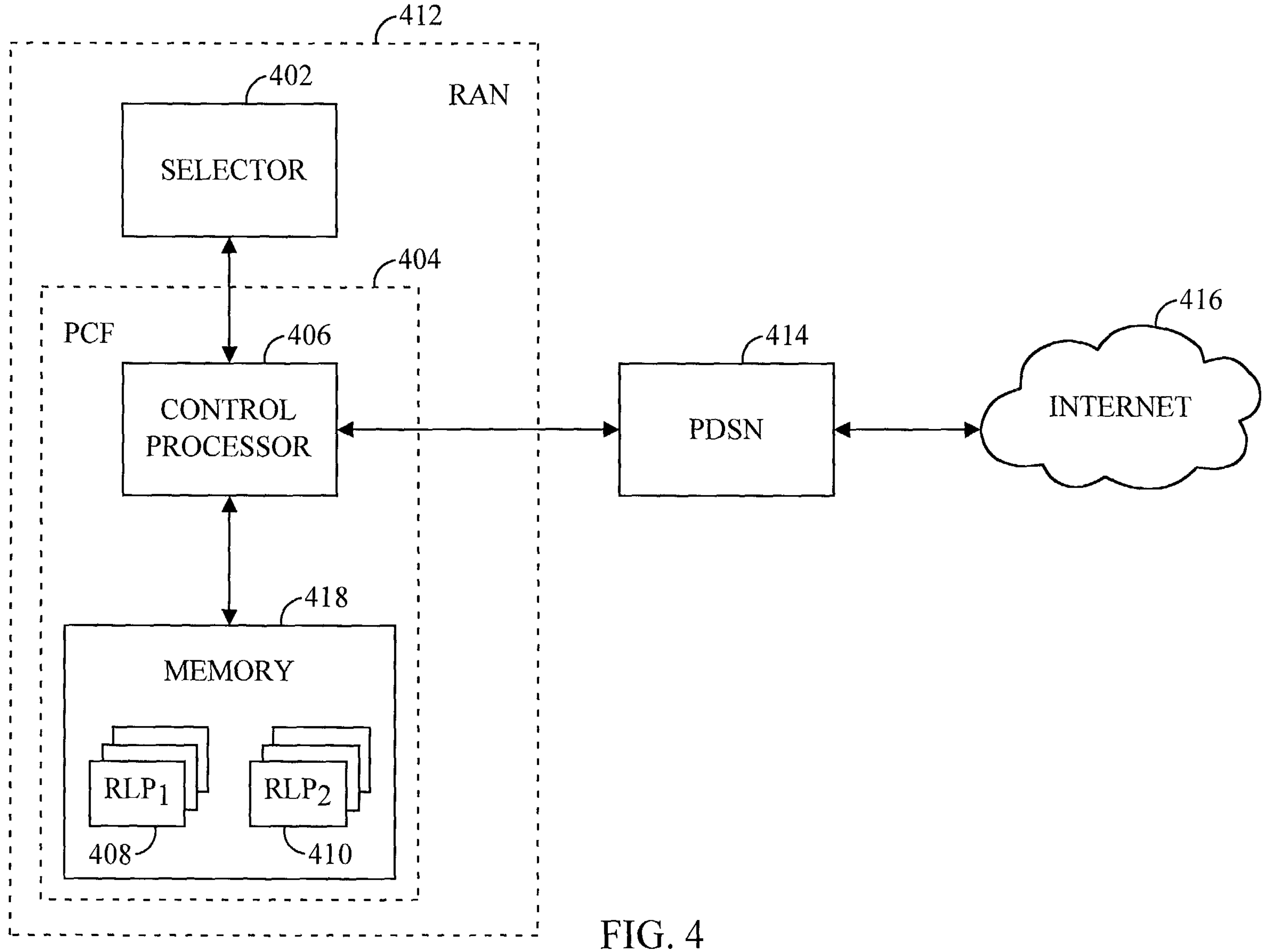
412
RAN
402
SELECTOR
404
PCF
406
CONTROL
PROCESSOR
418
MEMORY
RLP1
408
RLP2
410
414
PDSN
416
INTERNET

FIG. 4

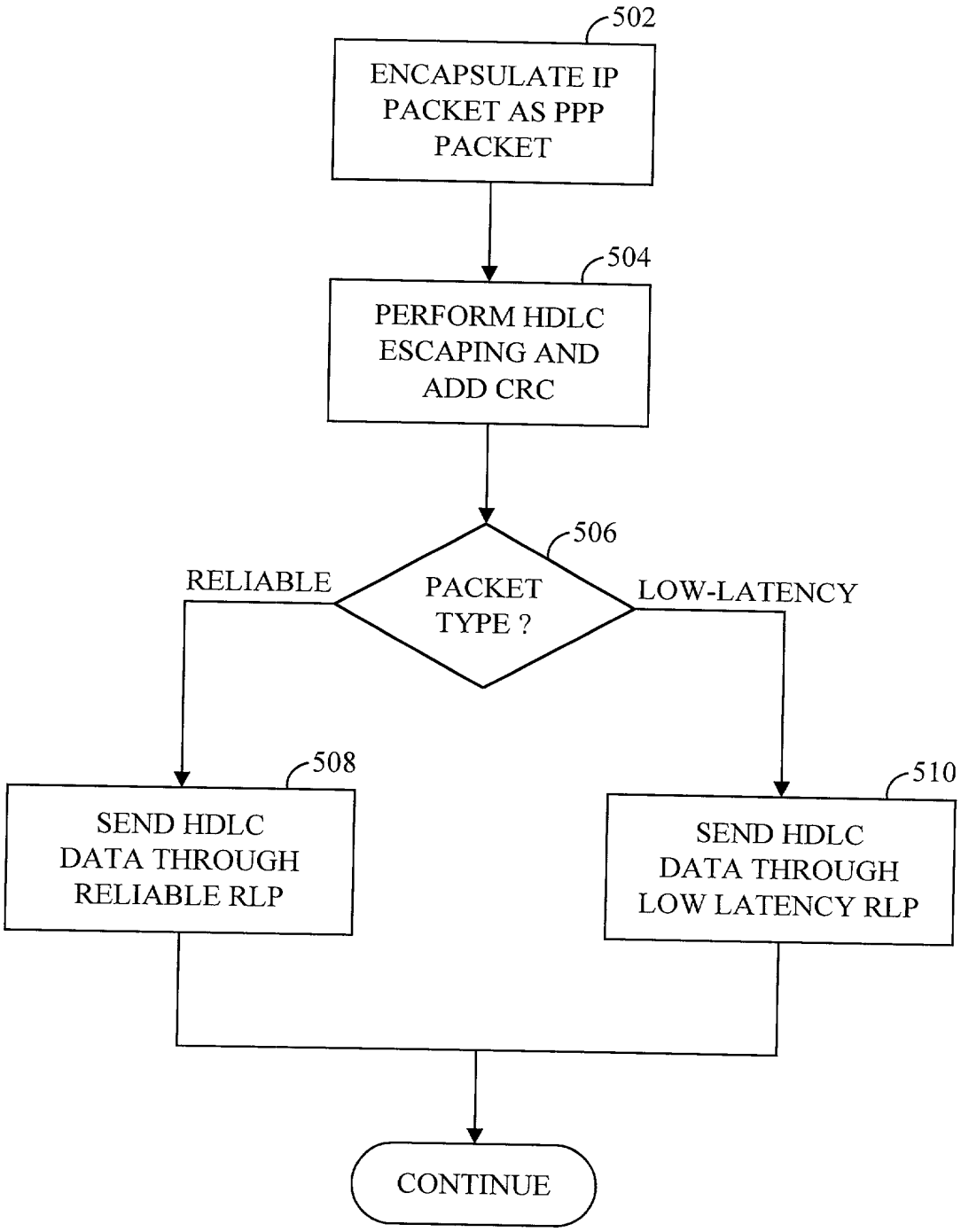
502
ENCAPSULATE IP PACKET AS PPP PACKET
504
PERFORM HDLC ESCAPING AND ADD CRC
506
PACKET TYPE ?
RELIABLE
LOW-LATENCY
508
SEND HDLC DATA THROUGH RELIABLE RLP
510
SEND HDLC DATA THROUGH LOW LATENCY RLP
CONTINUE

FIG. 5

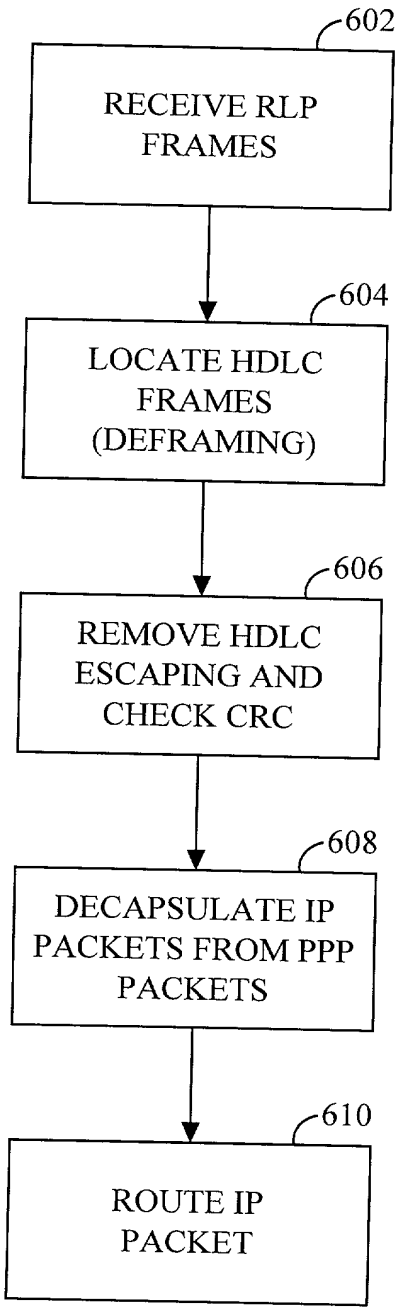
602
RECEIVE RLP FRAMES
604
LOCATE HDLC FRAMES (DEFRAMING)
606
REMOVE HDLC ESCAPING AND CHECK CRC
608
DECAPSULATE IP PACKETS FROM PPP PACKETS
610
ROUTE IP PACKET

FIG. 6